

TI-28917
1 of 7

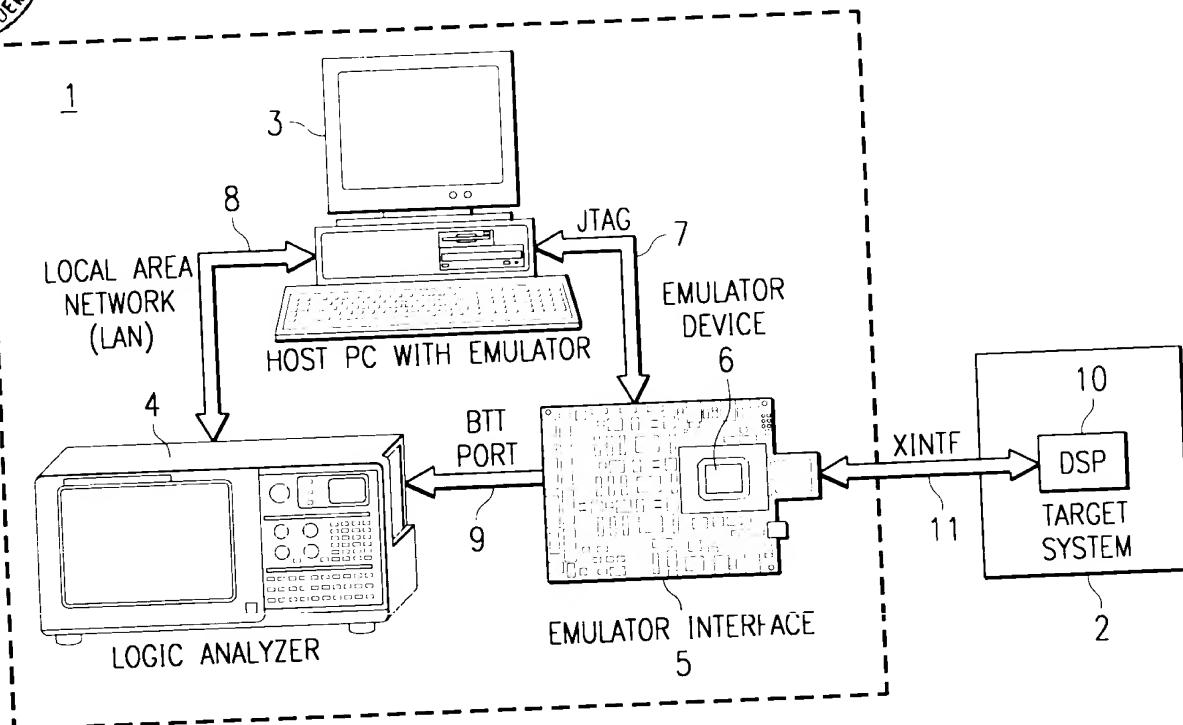


FIG. 1

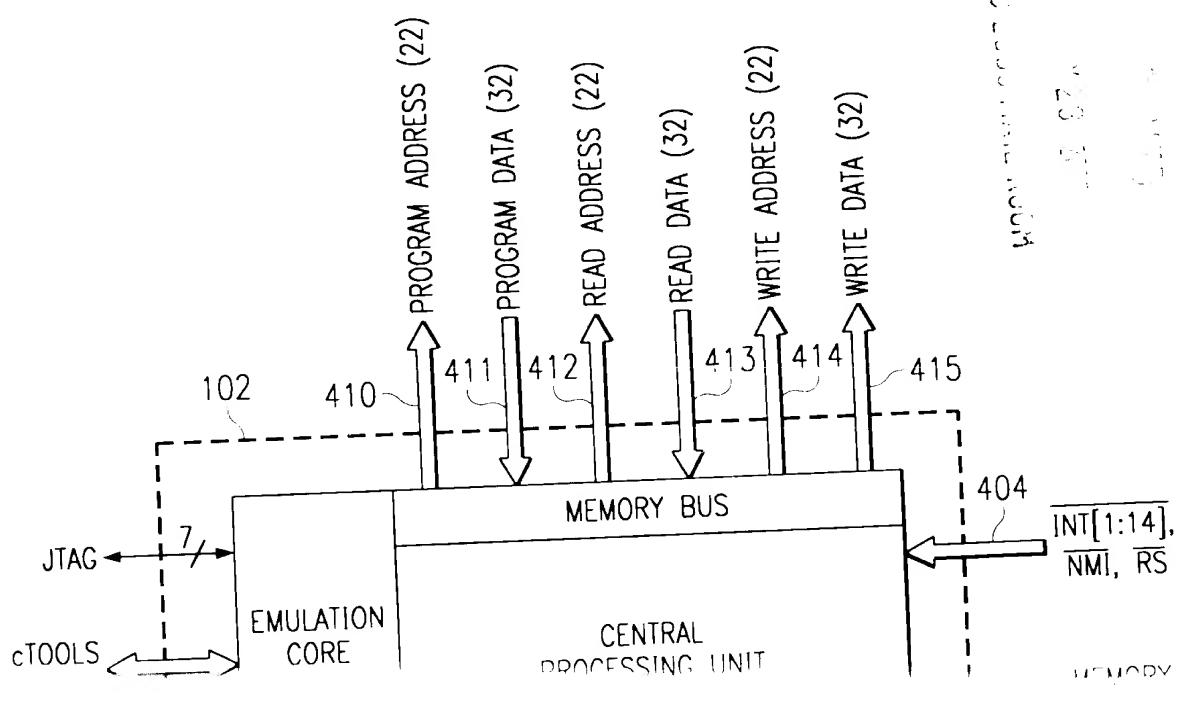
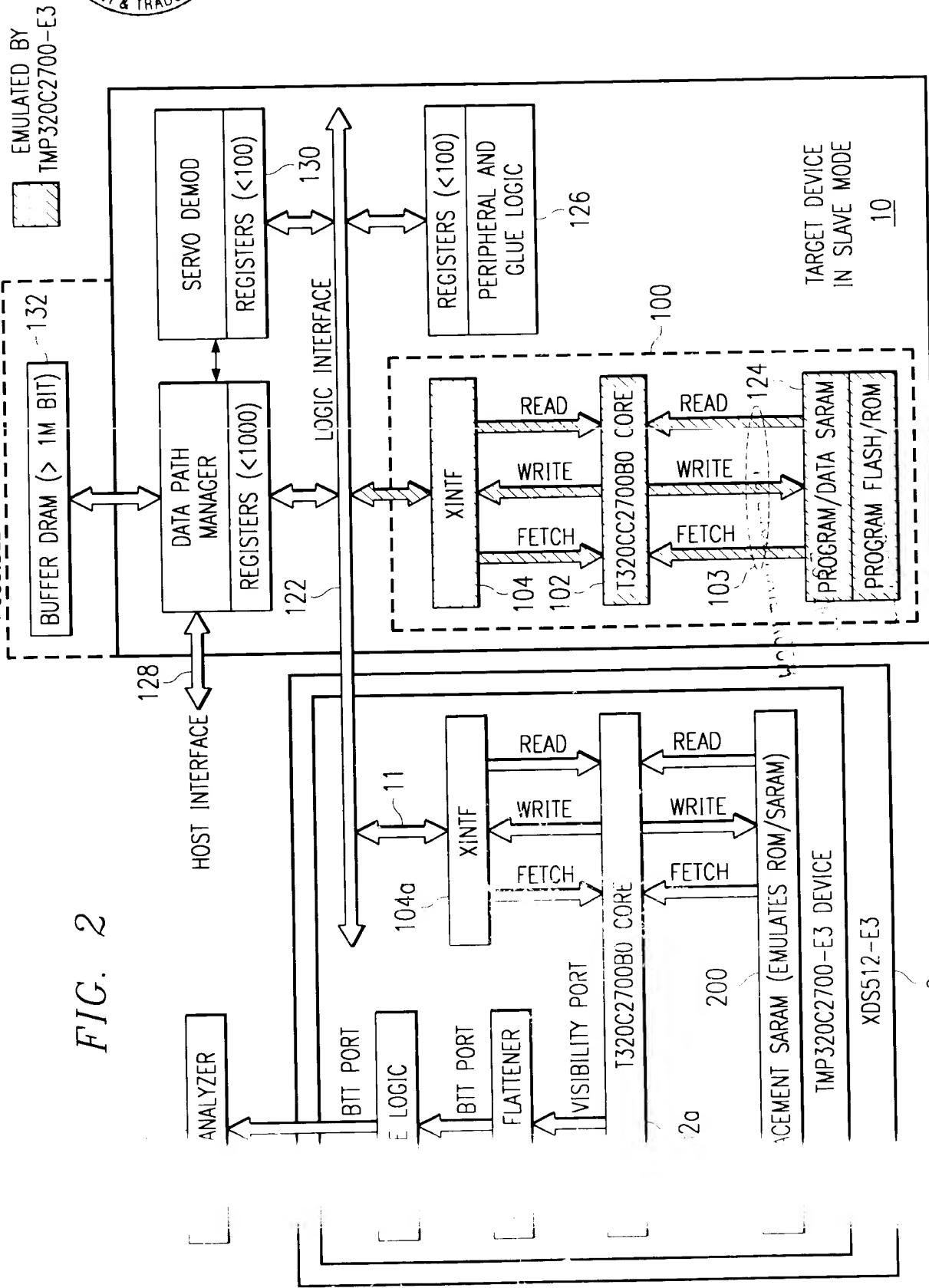


FIG. 2



FIG. 2

POSSIBLE FUTURE INTEGRATION



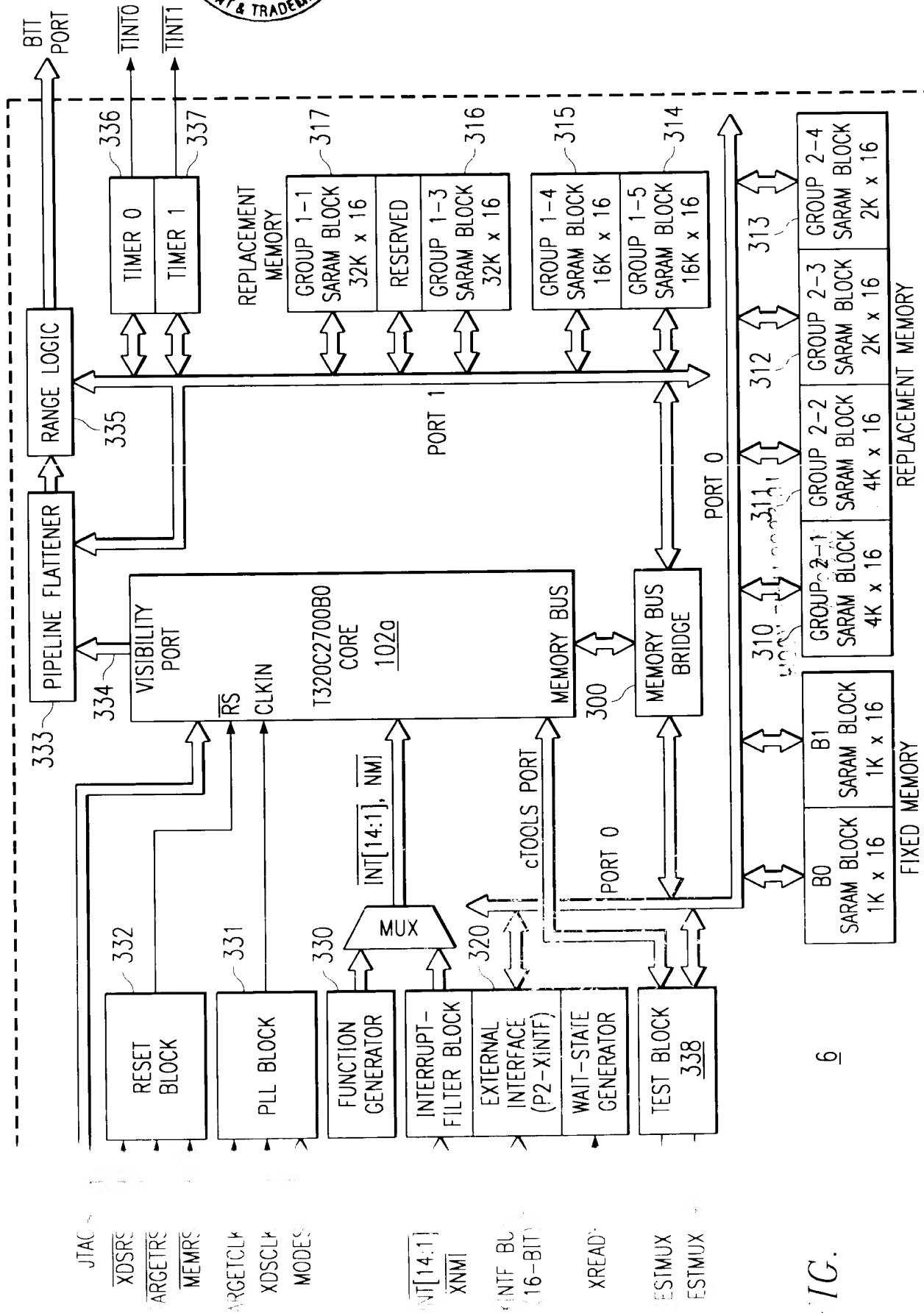




FIG. 5

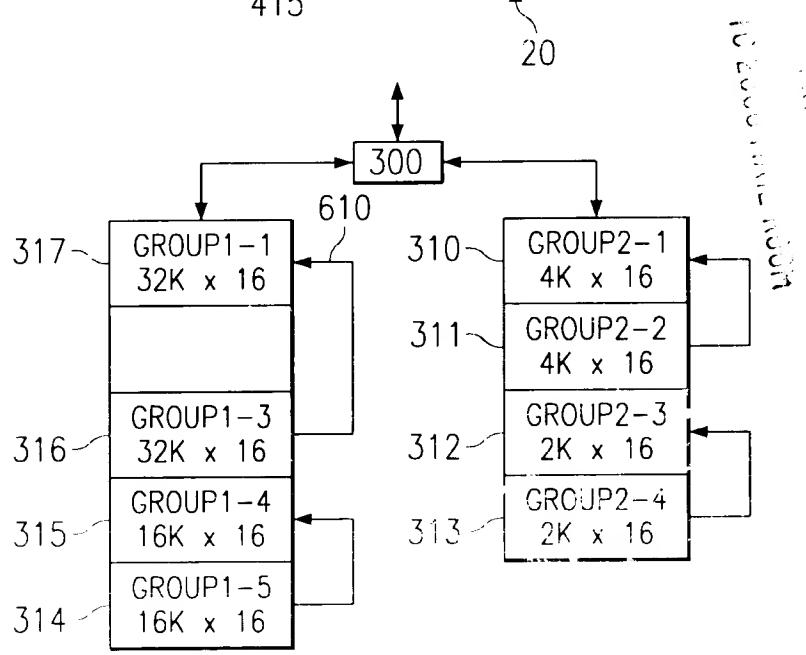
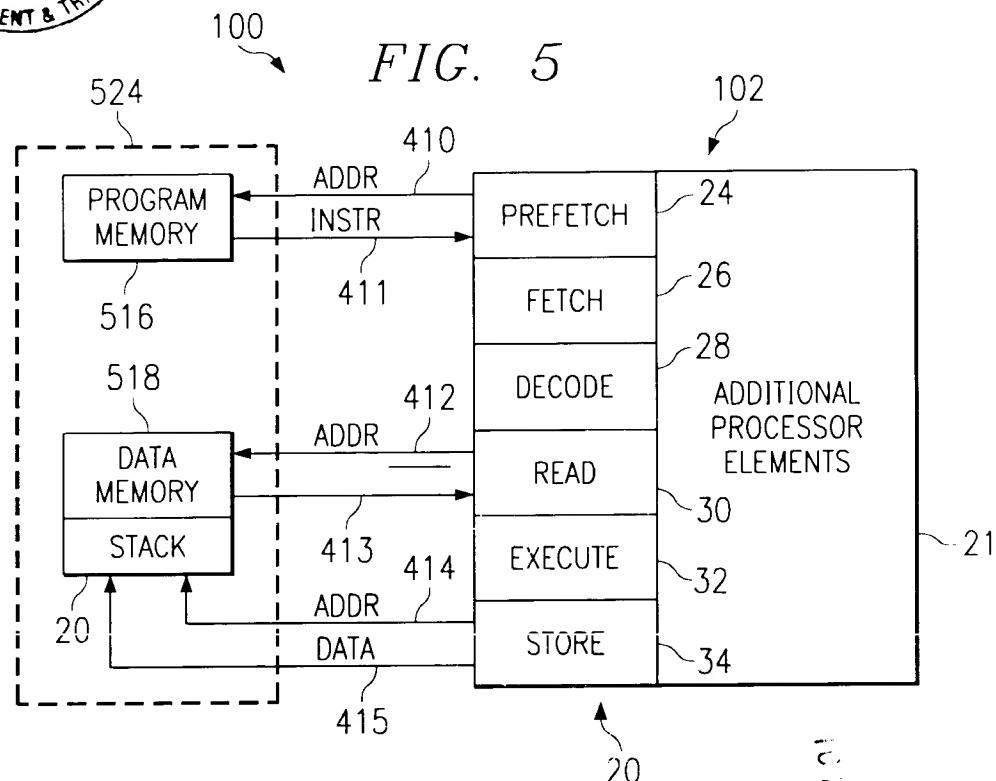
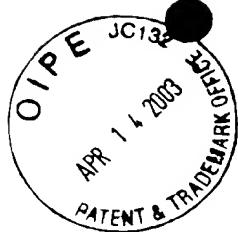


FIG. 6

ESTATE OR DISTRI [] 700
15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0



15															0
	RSVD	HIBANK	MERGE	ROM-MODE	RSVD	WS	R/W	X	R/W	X	R/W	DON	PON	R/W	800

NOTE: R=READ ACCESS, W=WRITE ACCESS, X=DON'T CARE

FIG.

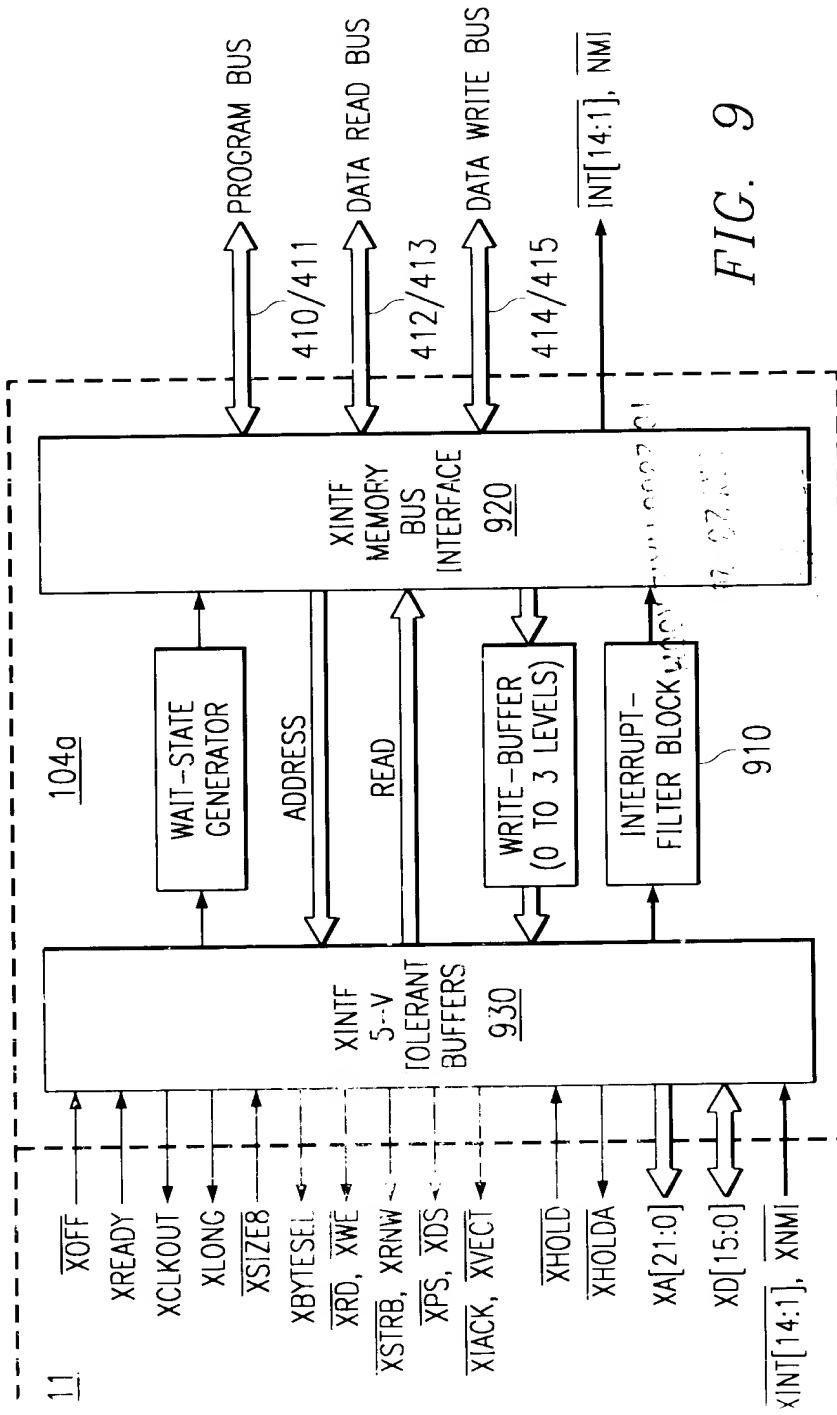


FIG. 9

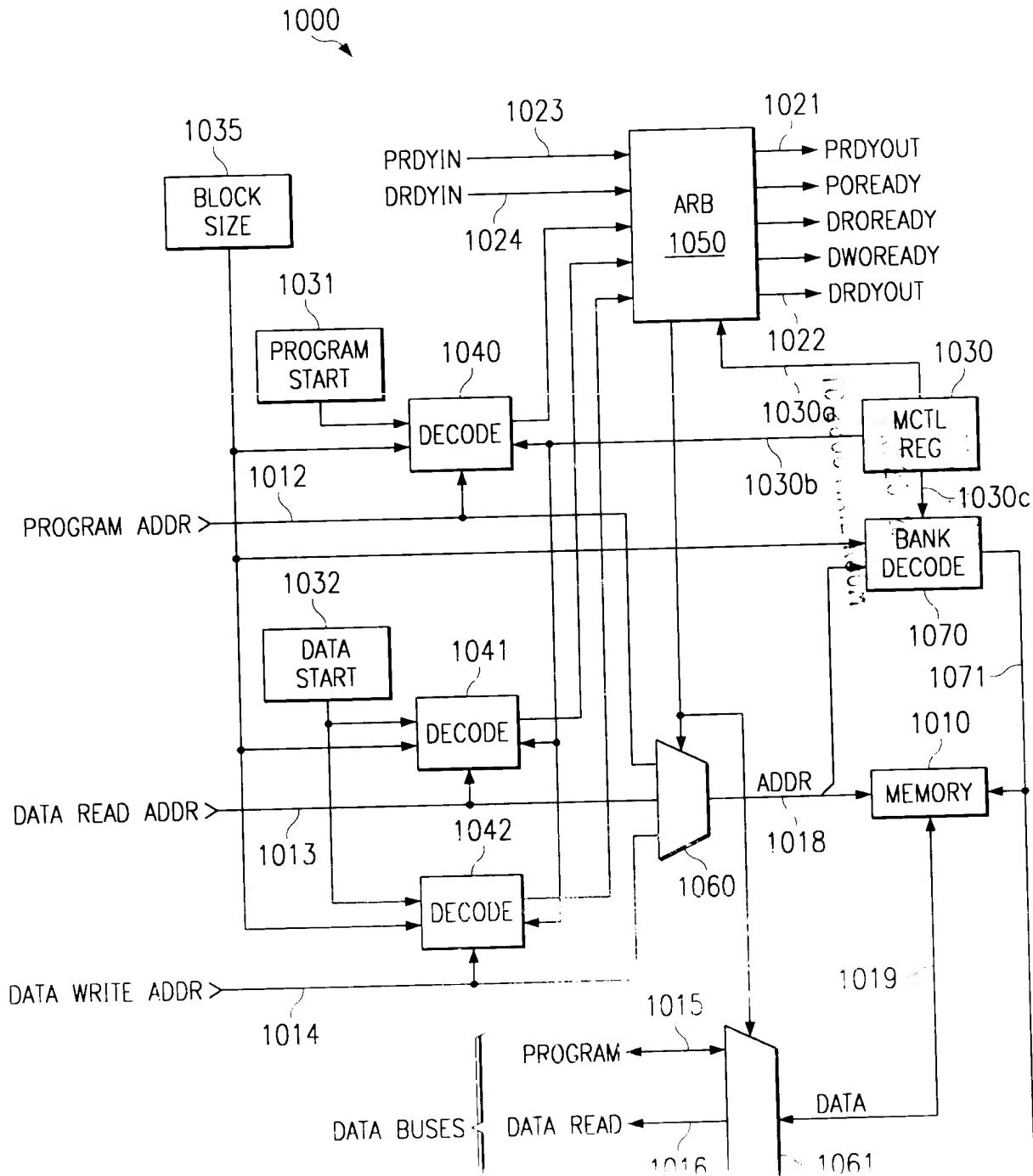


FIG. 10

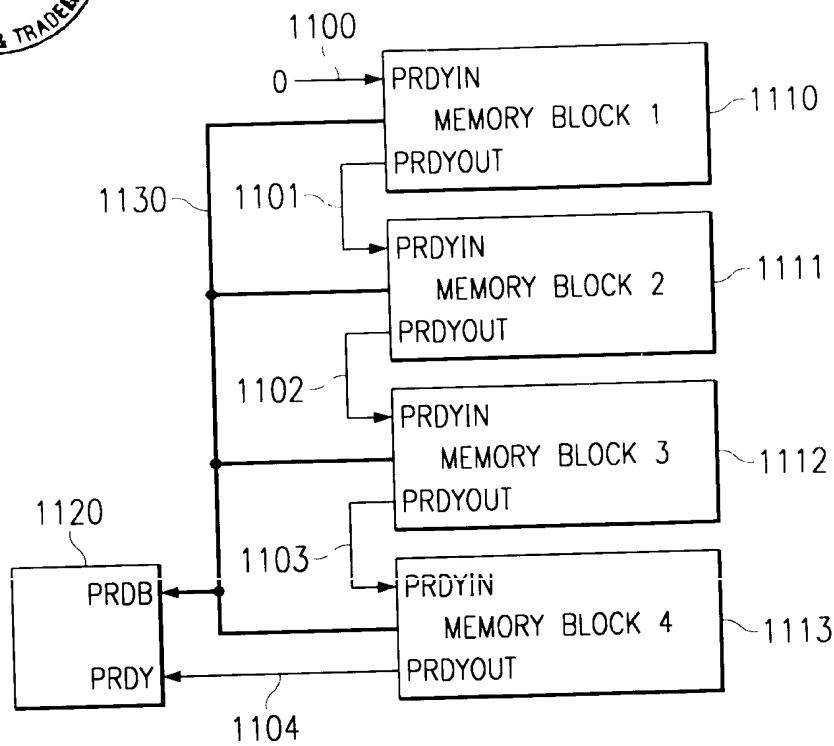


FIG. 11

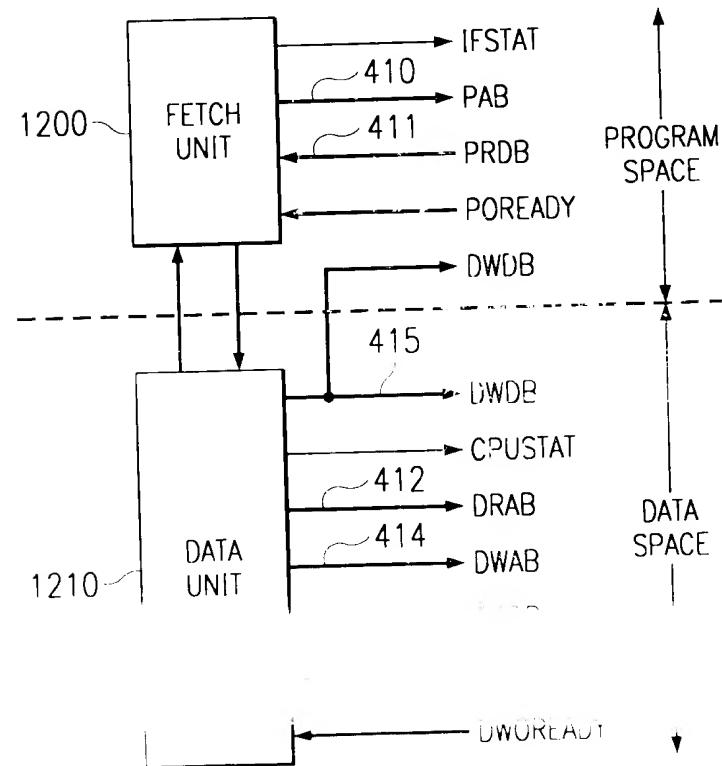


FIG. 12